PREFACE

This book examines the hardware and software features of the MCS-51 family of microcontrollers. The intended audience is college or university students of electronics or computer technology, electrical or computer engineering, or practicing technicians or engineers interested in learning about microcontrollers

The means to effectively fulfill that audience's informational needs were tested and refined in the development of this book. In its prototype form, *The 8051 Microcontroller* has been the basis of a fifth semester course for college students in computer engineering. As detailed in Chapter 10, students build an 8051 single-board computer as part of this course. That computer, in turn, has been used as the target system for a final, sixth semester "project" course in which students design, implement, and document a "product" controlled by the 8051 microcontroller and incorporating original software and hardware.

Since the 8051—like all microcontrollers—contains a high degree of functionality, the book emphasizes architecture and programming rather than electrical details. The software topics are delivered in the context of Intel's assembler (ASM51) and linker/locator (RL51).

It is my view that courses on microprocessors or microcontrollers are inherently more difficult to deliver than courses in, for example, digital systems, because a linear sequence of topics is hard to devise. The very first program that is demonstrated to students brings with it significant assumptions, such as a knowledge of the CPU's programming model and addressing modes, the distinction between an address and the content of an address, and so on. For this reason, a course based on this book should not attempt to follow strictly the sequence presented. Chapter 1 is a good starting point, however. It serves as a general introduction to microcontrollers, with particular emphasis on the distinctions between microcontrollers and microprocessors.

Chapter 2 introduces the hardware architecture of the 8051 microcontroller, and its counterparts that form the MCS-51 family. Concise examples are presented using short sequences of instructions. Instructors should be prepared at this point to introduce, in parallel, topics from Chapters 3 and 7 and Appendices A and C to support the requisite software knowledge in these examples. Appendix A is particularly valuable, since it contains in a single figure the entire 8051 instruction set.

Chapter 3 introduces the instruction set, beginning with definitions of the 8051's addressing modes. The instruction set has convenient categories of instructions (data transfer, branch, etc.) which facilitate a step-wise presentation. Numerous brief examples demonstrate each addressing mode and each type of instruction.

Chapters 4, 5, and 6 progress through the 8051's on-chip features, beginning with the timers, advancing to the serial port (which requires a timer as a baud rate generator),

and concluding with interrupts. The examples in these chapters are longer and more complex than those presented earlier. Instructors are wise not to rush into these chapters: it is essential that students gain solid understanding of the 8051's hardware architecture and instruction set before advancing to these topics.

Many of the topics in Chapter 7 will be covered, by necessity, in progressing through the first six chapters. Nevertheless, this chapter is perhaps the most important for developing in students the potential to undertake large-scale projects. Advanced topics such as assemble-time expression evaluation, modular programming, linking and locating, and macro programming will be a significant challenge for many students. At this point the importance of hands-on experience cannot be over-emphasized. Students should be encouraged to experiment by entering the examples in the chapter into the computer and observing the output and error messages provided by ASM51, RL51, and the object-to-hex conversion utility (OH).

Some advanced topics relating to programming methods, style, and the development environment are presented in Chapters 8 and 9. These chapters address larger, more conceptual topics important in professional development environments.

Chapter 10 presents several design examples incorporating selected hardware with supporting software. The software is fully annotated and is the real focus in these examples. The second edition includes two additional interfaces; a digital-to-analog output interface using an MC1408 8-bit DAC, and an analog-to-digital input interface using an ADC804 8-bit ADC. One of the designs in Chapter 10 is the SBC-51—the 8051 single-board computer. The SBC-51 can form the basis of a course on the 8051 microcontroller. A short monitor program is included (see Appendix G) which is sufficient to get "up and running." A development environment also requires a host computer which doubles as a dumb terminal for controlling the SBC-51 after programs have been downloaded for execution.

Many dozens of students have wire-wrapped prototype versions of the SBC-51 during the years that I have taught 8051-based courses to computer engineering students. Shortly after the release of the first edition of this text, URDA, Inc. (Pittsburgh, Pennsylvania) began manufacturing and marketing a PC-board version of the SBC-51. This has proven to be a cost-effective solution to implementing a complete lecture-plus-lab package for teaching the 8051 microcontroller to technology students. Contact URDA at 1-800-338-0517 for more information.

Finally, each chapter contains questions further exploring the concepts presented. This new edition includes 128 end-of-chapter questions—almost double the number in the first edition. A solutions manual is available to instructors from the publisher.

The book makes extensive use of, and builds on, Intel's literature on the MCS-51 devices. In particular, Appendix C contains the definitions of all 8051 instructions and Appendix E contains the 8051 data sheet. Intel's cooperation is gratefully acknowledged. I also thank the following persons who reviewed the manuscript and offered invaluable comments, criticism, and suggestions: Antony Alumkal, Austin Community College; Omer Farook, Purdue University—Calumet; David Jones, Lenoir Community College; Roy Seigel, DeVry Institute; and Chandra Sekhar, Purdue University—Calumet.

CONTENTS

1

	1.1 1.2	Introduction 1 Terminology 3			
	1.3	• •			
	1.4 Semiconductor Memory: RAM and ROM 5				
	1.5	The Buses: Address, Data, and Control 6			
	Input/Output Devices 7				
		1.6.1 Mass Storage Devices 71.6.2 Human Interface Devices 71.6.3 Control/Monitor Devices 7			
	1.7	Programs: Big and Small 8			
	1.8	Micros, Minis, and Mainframes 9			
	1.9	Microprocessors vs. Microcontrollers 10			
		 1.9.1 Hardware Architecture 10 1.9.2 Applications 10 1.9.3 Instruction Set Features 11 			
	1.10	New Concepts 12			
	1.11	Gains and Losses: A Design Example 13			
	Proble	ms 15			
2	HARD	WARE SUMMARY	17		
	2.1	MCS-51 [®] Family Overview 17			
	2.2	.2 Once Around the Pins 19			
		2.2.1 Port 0 20 2.2.2 Port 1 20			

INTRODUCTION TO MICROCONTROLLERS

3

2.2.3 Port 2 20

	2.2.4	Port 3 20		
	2.2.5	PSEN (Program Store Enable) 21		
	2.2.6	ALE (Address Latch Enable) 21		
	2.2.7	EA (External Access) 21		
	2.2.8	RST (Reset) 21		
	2.2.9	On-chip Oscillator Inputs 21		
	2.2.10	Power Connections 22		
2.3	I/O Por	t Structure 22		
2.4	Memor	y Organization 22		
	2.4.1	General Purpose RAM 23		
	2.4.2	Bit-addressable RAM 24		
	2.4.3	Register Banks 26		
2.5		Function Registers 26		
	2.5.1	Program Status Word 27		
	2.5.2	B Register 28		
	2.5.3			
	2.5.4			
		Port Registers 29		
	2.5.6	Timer Registers 30		
	2.5.7	ě		
	2.5.8	Interrupt Registers 31		
	2.5.9	Power Control Register 31		
		C		
2.6		Memory 32		
	2.6.1			
	2.6.2			
	2.6.3	Address Decoding 36		
	2.6.4	Overlapping the External Code and Data Spaces 36		
	2.7 8032/8052 Enhancements 37			
2.8 Reset Operation 38				
2.9	Summa	ry 39		
Proble	ms 40			
INSTR	IUCTION	N SET SUMMARY		
3.1	Introduc	ction 43		
3.2	Address	sing Modes 43		
	3.2.1	Register Addressing 44		
	3.2.2	Direct Addressing 45		
	3.2.3	Indirect Addressing 46		
	3.2.4	Immediate Addressing 47		
	3.2.5	Relative Addressing 47		
	3.2.6	Absolute Addressing 48		
		-		

CONTENTS

	3.2.7 Long Addressing 49 3.2.8 Indexed Addressing 49			
3.3				
	3.3.1 Arithmetic Instructions 50 3.3.2 Logical Instructions 51 3.3.3 Data Transfer Instructions 52 3.3.4 Boolean Instructions 54 3.3.5 Program Branching Instructions 55			
Proble	ms 58			
TIME	R OPERATION	63		
4.1	Introduction 63			
4.2	Timer Mode Register (TMOD) 64			
4.3	Timer Control Register (TCON) 66			
4.4	Timer Modes and the Overflow Flag 66			
	 4.4.1 13-Bit Timer Mode (Mode 0) 66 4.4.2 16-Bit Timer Mode (Mode 1) 67 4.4.3 8-Bit Auto-Reload Mode (Mode 2) 68 4.4.4 Split Timer Mode (Mode 3) 68 			
4.5	Clocking Sources 68			
	4.5.1 Interval Timing 68 4.5.2 Event Counting 68			
4.6	Starting, Stopping, and Controlling the Timers 69			
4.7	Initializng and Accessing Timer Registers 71			
	4.7.1 Reading a Timer "On the Fly" 71			
4.8	Short Intervals and Long Intervals 72			
4.9	8052 Timer 2 76			
	4.9.1 Auto-Reload Mode 77			
	4.9.2 Capture Mode 77			
4.10 4.11	Baud Rate Generation 78			
4.11 Probler	Summary 78 ns 79			
	L PORT OPERATION	81		
5.1				
5.2				
5.3	Modes of Operation 82			
	5.3.1 8-Bit Shift Register (Mode 0) 825.3.2 8-Bit UART with Variable Baud Rate (Mode 1) 84			

viii

		5.3.3 9-Bit UART with Fixed Baud Rate (Mode 2) 86				
		5.3.4 9-Bit UART with Variable Baud Rate (Mode 3) 87				
	5.4	,				
		5.4.1 Receiver Enable 87 5.4.2 The 9th Data Bit 87				
		5.4.3 Adding a Parity Bit 87				
		5.4.4 Interrupt Flags 88				
	5.5	Multiprocessor Communications 88				
	5.6	Serial Port Baud Rates 89				
		5.6.1 Using Timer 1 as the Baud Rate Clock 90				
	5.7	Summary 94				
	Proble	ms 94				
6	INTER	RRUPTS	97			
	6.1	Introduction 97				
	6.2	8051 Interrupt Organization 98				
		6.2.1 Enabling and Disabling Interrupts 98				
		6.2.2 Interrupt Priority 99				
		6.2.3 Polling Sequence 100				
	6.3	Processing Interrupts 100				
		6.3.1 Interrupt Vectors 102				
	6.4	Program Design Using Interrupts 102				
		6.4.1 Small Interrupt Service Routines 104				
		6.4.2 Large Interrupt Service Routines 104				
	6.5					
	6.6					
	6.7					
	6.8 Proble					
	riubie	112				
7	ASSE	MBLY LANGUAGE PROGRAMMING	117			
	7.1	Introduction 117				
	7.2	Assembler Operation 118				
		7.2.1 Pass One 119				
		7.2.2 Pass Two 119				
	7.3	Assembly Language Program Format 120				
		7.3.1 Label Field 120				
		7.3.2 Mnemonic Field 122				
		7.3.3 Operand Field 122 7.3.4 Comment Field 122				
		7.5.7 Comment Pictu 122				

	7.3.5	Special Assembler Symbols 122
	7.3.6	Indirect Address 123
	7.3.7	Immediate Data 123
	7.3.8	Data Address 124
	7.3.9	Bit Address 124
	7.3.10	Code Address 124
	7.3.11	Generic Jumps and Calls 124
7.4	Assemb	le-Time Expression Evaluation 125
	7.4.1	Number Bases 126
	7.4.2	Character Strings 126
	7.4.3	Arithmetic Operators 126
	7.4.4	
	7.4.5	Special Operators 127
	7.4.6	Relational Operators 127
	7.4.7	Expression Examples 128
	7.4.8	Operator Precedence 129
7.5	Assemb	ler Directives 129
	7.5.1	Assembler State Control 129
	7.5.2	Symbol Definition 130
	7.5.3	Storage Initialization/Reservation 132
	7.5.4	Program Linkage 135
	7.5.5	Segment Selection Directives 137
7.6	Assemb	ler Controls 138
7.7	Linker (Operations 140
7.8	Annotat	ed Example: Linking Relocatable Segments and Modules 141
	7.8.1	ECHO.LST 141
	7.8.2	IO.LST 146
	7.8.3	EXAMPLES.M51 147
7.9	Macros	148
	7.9.1	Parameter Passing 149
		Local Labels 150
	7.9.3	Repeat Operations 151
	7.9.4	Control Flow Operations 152
Problei	ms 152	·
	DAM CT	DUCTURE AND DECICAL
PHUG	HAM 91	RUCTURE AND DESIGN 155
8.1	Introduc	tion 155
8.2	Advanta	ges and Disadvantages of Structured Programming 157
8.3	The Thre	ee Structures 158
	8.3.1	Statements 158
	8.3.2	The Loop Structure 158
	8.3.3	

8.4 Pseudo Code Syntax 171

	8.5	Assembly Language Programming Style 174	
		8.5.1 Labels 174	
		8.5.2 Comments 175 8.5.3 Comment Blocks 176	
		8.5.4 Saving Registers on the Stack 176	
		8.5.5 The Use of Equates 176	
		8.5.6 The Use of Subroutines 176 8.5.7 Program Organization 179	
	8.6		
	Proble		
9	TOOL	S AND TECHNIQUES FOR PROGRAM DEVELOPMENT	18
	9.1	Introduction 181	
	9.2	The Development Cycle 181	
		9.2.1 Software Development 182	
		9.2.2 Hardware Development 183	
	9.3	Integration and Verification 185 9.3.1 Software Simulation 185	
		9.3.1 Software Simulation 185 9.3.2 Hardware Emulation 187	
		9.3.3 Execution from RAM 187	
		9.3.4 Execution from EPROM 188	
	9.4	9.3.5 The Factor Mask Process 188 Commands and Environments 189	
	9.5		
	Proble	·	
10	DESIG	ON AND INTERFACE EXAMPLES	193
	10.1	Introduction 193	
	10.2	The SBC-51 193	
	10.3		
	10.4	,	
	10.5	Loudspeaker Interface 205	
	10.6 10.7		
	10.8	• • •	
	10.9	• .	
	10.10	• .	
	Proble	ms 225	

2.1.1.227 (2.1)

CONTENTS

AP	PE	ND	IXE	S
----	----	----	-----	---

Α	Quick Reference Chart 229		
В	OPCODE Map 231		
C	Instruction Definitions 233		
D	Special Function Registers 277		
Ε	8051 Data Sheet 287		
F	ASCII Code Chart 303		
G	MON51—An 8051 Monitor Program 305		
H	Sources of 8051 Development Products 347		
BI	BIBLIOGRAPHY		
IN	INDEX		

1

INTRODUCTION TO MICROCONTROLLERS

1.1 INTRODUCTION

Although computers have only been with us for a few decades, their impact has been profound, rivaling that of the telephone, automobile, or television. Their presence is felt by us all, whether computer programmers or recipients of monthly bills printed by a large computer system and delivered by mail. Our notion of computers usually categorizes them as "data processors," performing numeric operations with inexhaustible competence.

We confront computers of a vastly different breed in a more subtle context performing tasks in a quiet, efficient, and even humble manner, their presence often unnoticed. As a central component in many industrial and consumer products, we find computers at the supermarket inside cash registers and scales; at home in ovens, washing machines, alarm clocks, and thermostats; at play in toys, VCRs, stereo equipment, and musical instruments; at the office in typewriters and photocopiers; and in industrial equipment such as drill presses and phototypesetters. In these settings computers are performing "control" functions by interfacing with the "real world" to turn devices on and off and to monitor conditions. **Microcontrollers** (as opposed to microcomputers or microprocessors) are often found in applications such as these.

It's hard to imagine the present world of electronic tools and toys without the microprocessor. Yet this single-chip wonder has barely reached its twentieth birthday. In 1971 Intel Corporation introduced the 8080, the first successful microprocessor. Shortly thereafter, Motorola, RCA, and then MOS Technology and Zilog introduced similar devices: the 6800, 1801, 6502, and Z80, respectively. Alone these integrated circuits (ICs) were rather helpless (and they remain so); but as part of a single-board computer (SBC) they became the central component in useful products for learning about and designing with microprocessors. These SBCs, of which the D2 by Motorola, KIM-1 by MOS Technology, and SDK-85 by Intel are the most memorable, quickly found their way into design labs at colleges, universities, and electronics companies.

A device similar to the microprocessor is the microcontroller. In 1976 Intel introduced the 8748, the first device in the MCS-48TM family of microcontrollers. Within a single integrated circuit containing over 17,000 transistors, the 8748 delivered a CPU,